

REMARKS

Claims 1, 3-6, 8-12, 14-18, 20, 22, 24, and 26 remain pending in the instant application. Claims 1, 3-6, 8-12, 14-18, 22, 24, and 26 presently stand rejected. Reconsideration of the pending claims is respectfully requested.

Claim Rejections – 35 U.S.C. § 103

Claims 1, 3-6, 8, 9, 18, and 20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,363,515 to Rajgopal et al. (“Rajgopal”) in view of U.S. Patent No. 5,828,579 to Beausang, in further view of “Validation and Test Generation for Oscillatory Noise in VLSI Interconnects” by Arani Sinha et al. (“Sinha”).

“To establish prima facie obviousness of a claimed invention, all the claim limitations must be taught or suggested by the prior art. All words in a claim must be considered in judging the patentability of that claim against the prior art.” M.P.E.P. § 2143.03.

The Examiner acknowledged, “the *Rajgopal et al.* reference does not expressly disclose the limitation concerning analysis of domino circuits wherein each domino circuit is simulated in a specific order and the next domino circuit is analyzed using as an input the output of the last domino circuit that was analyzed and reporting results of the simulation indicating whether any of the domino logic circuits are likely to generate an erroneous output.” *Office Action* mailed September 29, 2003, page 5, section 3.1. However, the Examiner relied upon Sinha as disclosing “reporting results of the simulation indicating whether any of the domino logic circuits are likely to generate an erroneous output.” *Office Action* mailed September 29, 2003, page 6, section 3.1.

Accordingly, Applicants submit herewith a Declaration pursuant to 37 C.F.R. § 1.131, swearing behind the November, 1999 publication date of Sinha. In light of the enclosed Declaration, Sinha may not be relied upon as prior art under § 103(a). Consequently, the prior art of record fails to teach or suggest all elements of independent claims 1, 4, 10, 15, and 17, as required under M.P.E.P. § 2143.03. Therefore, Applicants respectfully request that the instant § 103(a) rejections of claims 1, 4, 10, 15, and 17 be withdrawn.

Dependent claims 3, 5, 6, 8, 9, 11, 12, 14, 16, 18, 20, 22, 24, and 26 are nonobvious over the prior art of record for at least the same reasons as discussed above in connection with their respective independent claims, in addition to adding further limitations of their own. Accordingly, Applicants respectfully request that the instant § 103 rejections for claims 3, 5, 6, 8, 9, 11, 12, 14, 16, 18, 20, 22, 24, and 26 be withdrawn.

CONCLUSION

In view of the enclosed Declaration and foregoing remarks, Applicants believe the applicable rejections have been overcome and all claims remaining in the application are presently in condition for allowance. Accordingly, favorable consideration and a Notice of Allowance are earnestly solicited. The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.


CHARGE DEPOSIT ACCOUNT

It is not believed that extensions of time are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a). Any fees required therefore are hereby authorized to be charged to Deposit Account No. 02-2666. Please credit any overpayment to the same deposit account.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

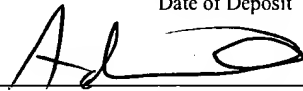
Date: Nov. 25, 2003


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FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

on November 25, 2003
Date of Deposit


Adrian Villarreal

November 25, 2003
Date